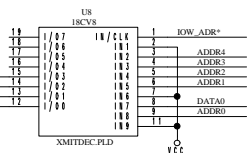
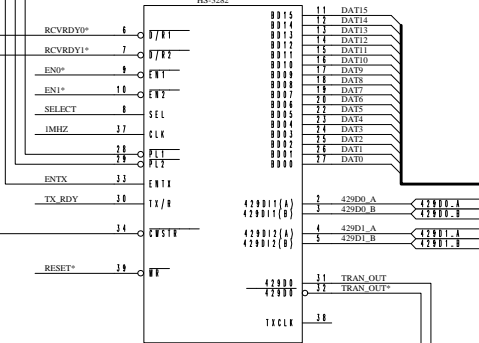


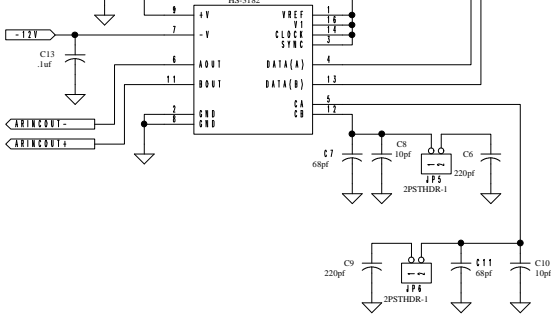
TRANSMIT DECODER



U1 HS-3282

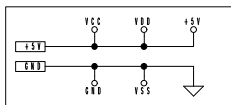


U22 HS-3182

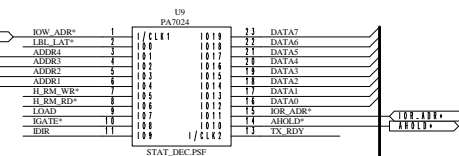


A 27 STATE CYCLE CONTROLS THE LOAD CYCLE OF THE BOARD.

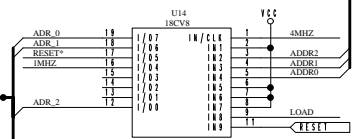
DATA COMING FROM THE RECEIVERS IS PLACED INTO THE DATA LATCHES SORTED AND STORED INTO RAM.



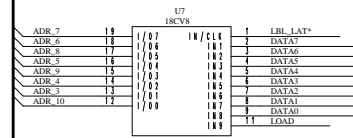
DECODER AND STATUS REGISTER



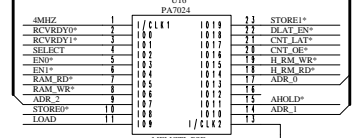
LOW ADDRESS



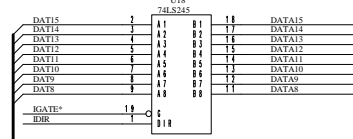
LABEL READ



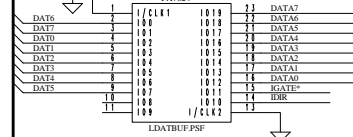
READBL.PLD



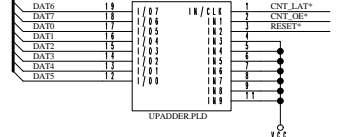
MEMCTL.PSF



DRIVER



LDATBUP.PSF



UPADDER.PLD



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